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Sheet 1

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Application Number	
Filing Date	September 16, 2003
First Named Inventor	Glenn M. Boles
Art Unit	
Examiner Name	
Attorney Docket Number	Boles 3-4-30

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/Habte Mered/	Date Considered	06/10/2007
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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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Sheet

2

of

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Attorney Docket Number

Boles 3-4-30

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/H.M./		Widmer, A.X. & Franaszek, P.A. (1983). "A DC-Balanced, Partitioned-Block, 8B/10B Trans..." IBM J Res & Dev. 27(5) 440-451	
/H.M./		Smith, D. R. "Digital Transmission Systems," 2d Ed. ISBN 0-442-00917-9, 272-281.	
/H.M./		Kiwimagi, R.G. (1976) "Encoding/Decoding for Magnetic Record Storage Apparatus." IBM Technical Disclosure Bull., 18(10) 3147-3149	

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